



PT:sj 20 March 2001

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<b>INFORMATION DISCLOSURE STATEMENT</b>  <b>BY APPLICANT</b>	Docket: 137.621CIP	Ser: 09/756,352
	Applicant: Ramfelt et al.	
	Filed: 1/8/2001	Group: 2665

**U.S. PATENT DOCUMENTS**

Init.*		Number	Date	Name	Class	Sub	Filed
		4,542,502	12/30/83	Levinson et al.			
		4,553,234	11/12/85	Brandsma et al.			
		4,587,651	5/4/83	Nelson et al.			
		5,276,682	1/4/94	Van As et al.			
		5,282,199	12/29/92	Herzberg et al.			
		5,420,986	5/30/95	Baldwin et al.			
		5,551,048	6/3/94	Steely, Jr.			
		5,654,969	6/23/94	Wilhelmson			
		5,838,687	11/27/96	Ramfelt			

**FOREIGN PATENT DOCUMENTS**

		Number	Date	Country	Class	Sub	
		EP 0 451 426 B1	02.11.94	Europe			

**OTHER DOCUMENTS**








		Article: <u>The DTM Gigabit Network</u> by Christer Bohm, Per Lindgren, Lars Ramfelt, Peter Sjodin; dated June 17, 1993; pages 1-20, 22-23.
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\*Examiner: Initial if considered, whether or not in conformance with MPEP 60; draw line through cite if not in conformance and not considered. Send copy.

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## OTHER DOCUMENTS (Continued)

		Article: <u>Fast Circuit Switching for the Next Generation of High Performance Networks</u> ; IEEE Journal on Selected Areas in Communications, Vol. 14, No. 2, February 1996, by Christer Bohm, Markus Hidell, Per Lindgren, Lars Ramfelt, and Peter Sjödin; pages 298-305.
		Article: <u>Multi-gigabit networking based on DTM - A TDM medium access technique with dynamic bandwidth-allocation</u> ; Computer Networks and ISDN Systems 24 (1992) 119-130 North-Holland; by Lars Gauffin, Lars Håkansson, and Björn Pehrson; pages 119-130.
		Article: <u>Incorporating continuation-of-message information, slot reuse, and fairness in DQDB networks</u> ; Computer Networks and ISDN Systems 24 (1992) 153-169 North Holland; by Subrata Banerjee and Biswanath Mukherjee; pages 153-169.
		Article: <u>MetaRing--A Full-Duplex Ring with Fairness and Spatial Reuse</u> ; IEEE Transactions on Communications, Vol. 41, No. 1, January 1993; by Israel Cidon and Yoram Ofek; pp. 110-120.
		Article: <u>A Simple Scheme for Slot Reuse Without Latency for a Dual Bus Configuration</u> ; by Oran Sharon and Adrian Segall, 1993 IEEE, 9 pages.
		Article: <u>A Study of Slot Reuse in Dual Bus Multiple Access Networks</u> by Mark W. Garrett and San-Qi Li; 1990 IEEE, pp. 617-629.
		Article: <u>L. Ramfelt</u> ; by Manoel A. Rodrigues, AT&T Bell Laboratories, Holmdel, NJ 07733; 1990 IEEE; pp. 636-643.

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